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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sreenivasan, Sidlgata V
App. No.: 10/706,537 GPAU: 2812
Filing Date: 11/12/2003 Examiner: Andre C. Stevenson
Dkt. No.: P110-52-03 Conf. No.: 6885
For: Formation of Conductive Templates Employing Indium Tin Oxide

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
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Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
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to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the

Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

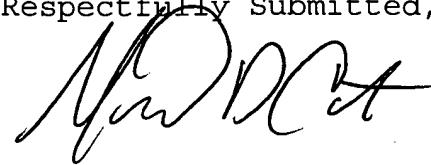
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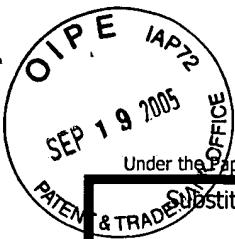
Signed: Katrina Prati
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Respectfully Submitted,



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PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>					
Sheet	1	of	6		
				APPLICATION NUMBER	10/706,537
				FILING DATE	11/12/2003
				FIRST NAMED INVENTOR	Sreenivasan et al.
				Group Art Unit	2812
				Examiner Name	Stevenson, Andre C.
				Attorney Docket Number	P110-52-03

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	D1	4,959,252		Bonnebat et al.	9/25/1990	
	D2	5,259,926		Kuwabara et al.	9/9/1993	
	D3	5,348,616		Hartman et al.	9/20/1994	
	D4	5,512,131		Kumar et al.	4/30/1996	
	D5	5,545,367		Bae et al.	8/13/1996	
	D6	5,776,748		Singhvi et al.	7/7/1998	
	D7	5,948,470		Harrison et al.	9/7/1999	
	D8	5,952,127		Yamanaka	9/14/1999	
	D9	6,051,345		Huang	4/18/2000	
	D10	6,218,316	B1	Marsh	4/17/2001	
	D11	4,201,800		Alcorn et al.	5/6/1980	
	D12	2004/0150129		Hougham et al.	8/5/2004	
	D13	2004/0202865		Homola et al.	10/14/2004	
	D14	5,937,758		Maracas et al.	8/17/1999	
	D15	6,753,131	B1	Rogers et al.	6/22/2004	
	D16	5,956,216		Chou	9/21/1999	
	D17	5,820,769		Chou	10/13/1998	

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		Office ³	Number ⁴			
D19	WO	87/02935		Long et al.	5/21/1987	
D20	JP	1-196749		Matsumoto et al.	8/8/1989	
D21	JP	02192045		Mineo et al.	7/27/1990	
D22	WO	01/90816		Heidari	11/29/2001	
D23	WO	02/22916		Pettersson et al.	3/21/2002	
D24	EP	1460738	A2	Gimkiewicz et al.	9/22/2004	
D25	JP	61-40845		Matsuo et al.	2/27/1986	
D26	JP	2-248480		Yoneda et al.	10/4/1990	
D27	JP	3-90345		Nobuyoshi et al.	4/16/1991	
D28	JP	9-278490		Toru et al.	10/28/1997	

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	D29	FEYNMAN, There's Plenty of Room at the Bottom, 12/1/1959	
	D30	XIA ET AL., Soft Lithography, Angew. Chem. Int. Ed., pp. 550 - 575 1/1/1998	
	D31	KRUG ET AL., Fine Patterning of Thin Sol-gel Films, Journal of Non-Crystalline Solids 147 & 148, pp. 447 - 450 1/1/1992	
	D32	NGUYEN, Asymmetric Fluid-Structure Dynamics in Nanoscale Imprint Lithography, The University of Texas at Austin, pp. 1 - 111 8/1/2001	
	D33	Abstract of Japanese Patent 02-92603, 8/12/2004	
	D34	Abstract of Japanese Patent 02-24848, 1/26/1990	
	D35	SOWAH, Diamond used to Break the Mould, Retrieved 9/2/2003 from URL: http://www.electronicstimes.com/story/OEG20020918S0003 9/18/2002	
	D36	Abstract of Japanese Patent 61-040845, 2/27/1986	
	D37	Abstract of Japanese Patent 02-248480, 10/4/1990	

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D38	Abstract of Japanese Patent 3-090345, 4/16/1991	
D39	Abstract of Japanese Patent 09-278490, 10/28/1997	
D40	Abstract of Japanese Patent 61-040845, 2/27/1986	
D41	Abstract of Japanese Patent 02-248480, 10/4/1990	
D42	GEHOSKI ET AL., Indium Tin Oxide Template Development for Step and Flash Imprint Lithography, SPIE Microlithography Conference 2/1/2005	
D43	DAUKSHER ET AL., Repair of Step and Flash Imprint Lithography Templates, J. Vac. Sci. Technol. B 22(6), pp. 3306-3311 11/1/2004	
D44	NORDQUIST ET AL., Image Placement Issues for ITO-based Step and Flash Imprint Lithography Templates, J. Vac. Sci. Technol. B, pp. 695-701 3/1/2004	
D45	DAUKSHER ET AL., Step and Flash Imprint Lithography Template Characterization, from an Etch Perspective, J. Vac. Sci. Technol. B 21(6), pp. 2771-2776 11/1/2003	
D46	RESNICK ET AL., Imprint Lithography: Lab Curiosity or the Real NGL?, SPIE Microlithography Conference 2/1/2003	

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	D47	MANCINI ET AL., Analysis of Critical Dimension Uniformity for Step and Flash Imprint Lithography, SPIE Microlithography Conference 2/1/2003		
	D48	MANCINI ET AL., Hydrogen Silsesquioxane for Direct Electron-Beam Patterning of Step and Flash Imprint Lithography Templates, J. Vac. Sci. Technol. B 20(6), pp. 2896-2901 11/1/2002		
	D49	DAUKSHER ET AL., Characterization of and Imprint Results Using Indium Tin Oxide-Based Step and Flash Imprint Lithography Templates, J. Vac. Sci. Technol. B 20(6), pp. 2857-2861 11/1/2002		
	D50	NORDQUIST ET AL.Q, Critical Dimension and Image Placement Issues for Step and Flash Imprint Lithography Templates, 22nd Annual BACUS Symposium on Photomask Technology, Monterey, CA 9/1/2002		
	D51	BAILEY ET AL., Template Fabrication Schemes for Step and Flash Imprint Lithography, Microelectronic Engineering, 61-62, pp. 461-467 1/1/2002		
	D52	RESNICK ET AL., High Resolution Templates for Step and Flash Imprint Lithography, SPIE Microlithography Conference 2/1/2002		
	D53	RESNICK ET AL., New Methods for Fabricating Step and Flash Imprint Lithography Templates, NIST-SPIE Conference on Nanotechnology 9/1/2001		

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